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•	Application No.	Applicant(s)
Notice of Allowability	10/673,081	JOYCE ET AL.
	Examiner	Art Unit
	Tuan V. Thai	2186
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31:	i (OR REMAINS) CLOSED i) or other appropriate comm RIGHTS. This application is	n this application. If not included
1. \boxtimes This communication is responsive to <u>amendment filed 8/1</u>	<u>6/2007</u> .	•
2. The allowed claim(s) is/are 1, 3-12 and 14-22 renumbered	d as 1-20 respectively.	
3. ☐ Acknowledgment is made of a claim for foreign priority u a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have	e been received.	
2. Certified copies of the priority documents have		
3. Copies of the certified copies of the priority do	ocuments have been receive	d in this national stage application from the
International Bureau (PCT Rule 17.2(a)). * Certified copies not received:		
•	•	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file MENT of this application.	e a reply complying with the requirements
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv 	nitted. Note the attached EX. es reason(s) why the oath o	AMINER'S AMENDMENT or NOTICE OF r declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.	
(a) including changes required by the Notice of Draftspers		w (PTO-948) attached
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment o	r in the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the sheet is the sheet of the sheet in the sheet is the sheet of the sheet is the sheet of the sheet is the sheet of the shee	.84(c)) should be written on t the header according to 37 Ci	he drawings in the front (not the back) of FR 1.121(d).
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 	Sit of BIOLOGICAL MAT FOR THE DEPOSIT OF BIO	ERIAL must be submitted. Note the OLOGICAL MATERIAL.
Attachment(s)		
1. Notice of References Cited (PTO-892)		formal Patent Application
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 	Paper No. 7.	/Mail Date Amendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's	Statement of Reasons for Allowance
of Biological Material	9.	- (Llanvan)
	·	PRIMARY EXAMINER
		V/0 Rept 2/00

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Attorney's Docket No.: 015114-066300US

IN THE UNITED STATES PATENT AND

TRADEMARK OFFICE

In re application of: Joyce et al. Group: 2186

Serial No.: 10/673,081 Examiner: Tuan Thai

For: TECHNIQUES FOR SEQUENTIALLY TRANSFERRING DATA FROM A

MEMORY DEVICE THROUGH A PARALLEL INTERFACE.

1. This action is responsive to communication filed August 16, 2007. Claims 1, 2-12 and 14-22 are now allowed. Claims 2 and 13 have been canceled.

REASONS FOR ALLOWANCE

2. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach nor suggest, either alone or in combination, all the limitations of the amended claims of the current invention (claims 1 and 12). The discussion of the reasons for allowance shall be directed to claim 1 in which the Examiner shall designate as the primary invention in this application; however, the reasons for allowance will also apply to other indicated independent claim (claim 12).

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Particularly, the prior arts of record do not teach nor suggest all the combined limitations in each claim separately including a method for transferring data in parallel from an external memory device to an integrated circuit comprises transferring a start read address from the integrated circuit via data lines to the external memory device; providing a clock signal generated by the integrated circuit to the external memory device; sequentially generating read addresses in response to the clock signal beginning with the start read address using an address counter in the external memory device; reading data stored in the external memory device at the read addresses; transferring the data in parallel from the external memory device to the integrated circuit; resetting a read address to the start read address when an output enable signal generated by the integrated circuit has a first voltage, wherein the address counter increments the read address in response to the clock signal when the output enable signal has a second voltage; similarly for other independent claim 12. In light of the foregoing, claims 1 and 12 of the present application are found to be patentable over the prior arts.

Claims 3-6 and 14-22 further limit the allowable independent claims 1 and 12. These claims are therefore allowable for the same reason as set forth above.

Any comments considered necessary by Applicant must be

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submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan V. Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 3:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew M. Kim can be reached on (571)-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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TVT/September 25, 2007

PRIMARY EXAMINER
Group 2100